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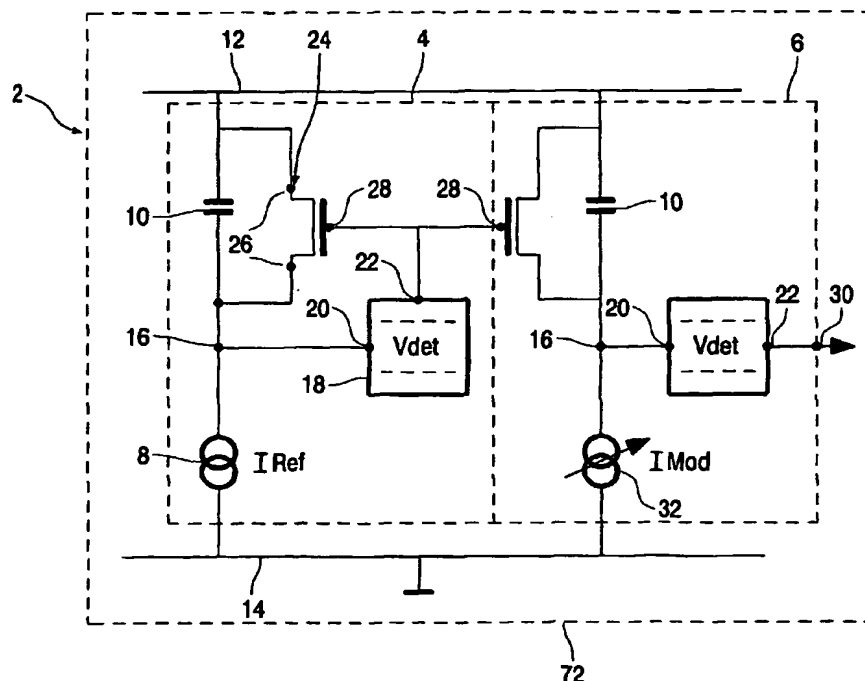
(75) Inventor/Applicant (for US only): **STEELE, Andrew** [GB/GB]; c/o Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB).(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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(54) Title: PULSE WIDTH MODULATION SIGNAL GENERATOR



(57) Abstract: A signal generator has matched reference (4) and modulation (6) ramp circuits, each including current source (8, 32), capacitance (10) charged by the current source, voltage detector (18) and switch (24). The output of one of the voltage detectors (8) controls both switches (24), and the output of the other voltage detector (18) is used as the output.



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**A. CLASSIFICATION OF SUBJECT MATTER**  
 IPC 7 H03K7/08

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

 Minimum documentation searched (classification system followed by classification symbols)  
 IPC 7 H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4 633 168 A (VENEMA HARRY J) 30 December 1986 (1986-12-30) column 1, line 64 -column 3, line 36; figures 1,2 column 4, line 38 - line 41	1,2,4,5
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X	US 4 404 481 A (IDE TOSHIMINE ET AL) 13 September 1983 (1983-09-13) column 5, line 4 - line 15; figures 6,8,9,14 column 5, line 44 -column 7, line 65	1,2
A	---	3-10
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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	PATENT ABSTRACTS OF JAPAN vol. 008, no. 275 (P-321), 15 December 1984 (1984-12-15) -& JP 59 142418 A (MATSUSHITA DENKI SANGYO KK), 15 August 1984 (1984-08-15) abstract; figure 2 ---	1-10
A	US 5 508 664 A (RIZZO RAYMOND P) 16 April 1996 (1996-04-16) column 1, line 63 -column 2, line 35; figures 2,3,6,7 column 3, line 31 -column 5, line 19 column 5, line 58 -column 6, line 12 ---	1-10
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A	AZAIS F ET AL: "A low-cost adaptive ramp generator for analog BIST applications" IEEE, 29 April 2001 (2001-04-29), pages 266-271, XP010542397 the whole document -----	1-10

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No  
PCT/JP 03/02862

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